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CONFIRMATION NO. 6232

Bib Data Sheet

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|-----------------------------|-----------------------------------|--------------|------------------------|----------------------------------|
| SERIAL NUMBER 10/098,535 | FILING DATE 03/18/2002 RULE | CLASS 716 | GROUP ART UNIT 2812 | ATTORNEY DOCKET NO. 020348 |
|-----------------------------|-----------------------------------|--------------|------------------------|----------------------------------|

APPLICANTS

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** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

JAPAN 2001-309053 10/04/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/16/2002

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|--|---|---------------------------|------------------------|--------------------------|------------------------------|
| Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>TM</i> Initials <i>TM</i> | STATE OR COUNTRY JAPAN | SHEETS DRAWING 3 | TOTAL CLAIMS 16/20 | INDEPENDENT CLAIMS 2/6 |
|--|---|---------------------------|------------------------|--------------------------|------------------------------|

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TITLE

Semiconductor integrated circuit having a plurality of circuit regions where different power supply voltages are used and method of manufacturing the same

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